

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

First Named Inventor: HANZAWA, SATORU Art Unit: 2827

Appln. No.: 10/579,911 Examiner: Hidalgo,
Fernando N.

Filed: April 9, 2007 Conf. No.: 6903

For: SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE

* * *

STATEMENTS OF SUBSTANCE OF INTERVIEWS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants' representative acknowledges with appreciation the in-person Interview on June 10, 2009 and the telephone Interview on June 23, 2009, both with Examiner Hidalgo.

For the most part, the substance of the aforementioned in-person Interview is accurately reflected in the Examiner's Interview Summary mailed June 24, 2009. Applicants, however, note that an agreement with respect to the claims was not reached as indicated by the Interview Summary, as Applicants maintain that Kengeri and the Lines references are not properly combinable.

With respect to the telephone Interview on June 24, 2009, the Examiner agreed to amend the claims to place the application in condition for allowance. Specifically, Applicants' representative authorized via Examiner's Amendment cancellation of Claims 11-17, without acceding to the outstanding rejections, and changing of the dependency for Claim 20 such that it depends from allowable Claim 18.

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 (XA-10573) any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and has not been separately requested, such extension is hereby requested.

Respectfully submitted,

MWS:PLM:vy

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June 24, 2009

By:



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